

[10191/2231]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : PLIKAT et al.

Serial No. : 10/031,526

Filed : May 9, 2002

For : BI-DIRECTIONAL SEMICONDUCTOR COMPONENT

Examiner : S. Loke

Art Unit : 2811

Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with
the United States Postal Service as first class mail in an envelope
addressed to: Assistant Commissioner for Patents, Washington,
D.C. 20231, on

Date: 2/11/03

Signature

LD Magist

DERVIS MAGISTRE
KENYON & KENYON

Reg. 41,172

RECEIVED
FEB 24 2003
TECHNOLOGY CENTER 2800

AMENDMENT TRANSMITTAL

SIR:

Please find an Election With Traverse/Amendment transmitted herewith for
filing in the above-identified patent application.

No fee is believed to be required. However, if any fee is required, please use
Deposit Account No. 11-0600. A duplicate copy of this transmittal letter is enclosed for that
purpose.

Respectfully submitted,

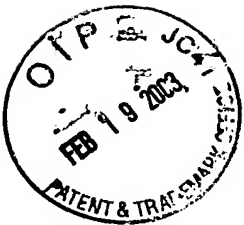
KENYON & KENYON

By: *LD Magist* (Reg. No. 41,172)

Dated: 2/11/03

By: *Richard L. Mayer*
Richard L. Mayer
Reg. No. 22,490

One Broadway
New York, NY 10004
(212) 425-7200



[10191/2231]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : PLIKAT et al.

Serial No. : 10/031,526

Filed : May 9, 2002

For : BI-DIRECTIONAL SEMICONDUCTOR COMPONENT

Examiner : S. Loke

Art Unit : 2811

Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with
the United States Postal Service as first class mail in an envelope
addressed to: Assistant Commissioner for Patents, Washington,
DC 20231, on

Date: 2/11/03

Reg. 41,172

Signature

D. Magistre
DERVIS MAGISTRE
KENYON & KENYON

ELECTION WITH TRAVERSE/AMENDMENT

SIR:

In response to the restriction requirement dated
December 12, 2002, entry of the following amendment and election
is respectfully requested.

IN THE CLAIMS

Please amend claims 15 and 16 as set forth below:

- sub 3*
B1
15. (Amended) The method according to claim 14, wherein:
the time-staggered activation is performed by
interconnecting a capacitor.
16. (Amended) The method according to claim 14, wherein:
the time-staggered activation is performed by a
time control.

ELECTION

Applicants elect, with traverse, the claims of Group

03/05/2003 DS/SFAT 00000005 110500 10031526 12.

01 FC:1252 410.00 CH